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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: ASANO et al.

Serial No.: 10/797,081

Filed: 3/11/2004

Title: SEMICONDUCTOR DEVICE AND
METHOD OF MANUFACTURING THE
SAME

Atty. Dkt.: 01-592

Art Unit: 2815

Examiner: Landau

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: 14 February 2006

CERTIFICATE OF FACSIMILE TRANSMISSION

I hereby certify that this correspondence is being facsimile transmitted to the USPTO (Fak. No. 571-273-8300) on 14 February 2006
Typed Name: James E. Barlow.Signature: James E. BarlowSUPPLEMENTAL AMENDMENT UNDER 37 CFR 1.116

Sir:

In response to the office action mailed 1 November 2005 and further to the amendment
filed on 30 January 2006, please consider the remarks that begin on page 2 of this paper.

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Serial No. 10/797,081

Attorney Docket No. 01-592

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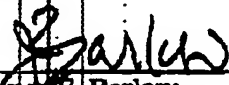
which an error is output by the ladder circuit or the pair property deteriorates (5% - 17% pair dispersion). In the present invention, even if the resistive film thickness is 100-500 Angstroms, the pair defined by the under shape is restrained to a pair dispersion of less than 2% as shown in Fig. 3C.

It was asserted in the office action that the applicants have not properly asserted the superior results achieved by the recited ranges. In Figs. 2A and 3C, the applicants clearly show the superior results achieved by the recited taper angle and metal fin space, respectively.

In view of the foregoing, the applicants submit that this application is in condition for allowance. A timely notice to that effect is respectfully requested. If questions relating to patentability remain, the examiner is invited to contact the undersigned by telephone.

Please charge any unforeseen fees that may be due to Deposit Account No. 50-1147.

Respectfully submitted,


James E. Barlow
Reg. No. 32,377

Posz Law Group, PLC
12040 South Lakes Drive, Suite 101
Reston, VA 20191
Phone 703-707-9110
Fax 703-707-9112
Customer No. 23400